



units. The claimed computation units perform computationally intensive operations, such as channel decoding, equalization, chip-rate processing, synchronization, channelization, parameter estimation, etc. (See specification, page 7, lines 13-25.) Since Dao does not suggest computation units, Dao does not suggest the claimed channel pooling signal processor. Thus, the claims are patentable over the applied references for at least this reason.

With further regard to claims 3-5, there is no disclosure or suggestion in Dao of a second channel pooling signal processor (claim 3), or that the computation units are heterogeneous (claim 4) or homogeneous (claim 5). Thus, claims 3-5 are patentable for these additional reasons.

Further, claim 9 is specifically directed to a base station transceiver. There is no disclosure or suggestion in the applied references, alone or in combination, of a base station transceiver. Claim 9 is therefore further patentable for this additional reason.

Newly added dependent claims 11-22 are directed to further details of the computation units. More specifically, the new claims recite that the computation units are flexibly configured and connected to perform any one of several different transceiver functions (claims 11, 14, 17, and 20), are configured to perform one or more of downconversion, dechannelization, demodulation, decoding, equalization, despreading, encoding, modulation, spreading, and diversity processing (claims 12, 15, 18, and 21), and support time-division, code-division, and/or frequency division processing (claims 13, 16, 19, and 22). The applied references do not teach or suggest any of these features, and thus these claims are patentable over the applied references for these additional reasons.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue.

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Respectfully submitted,

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